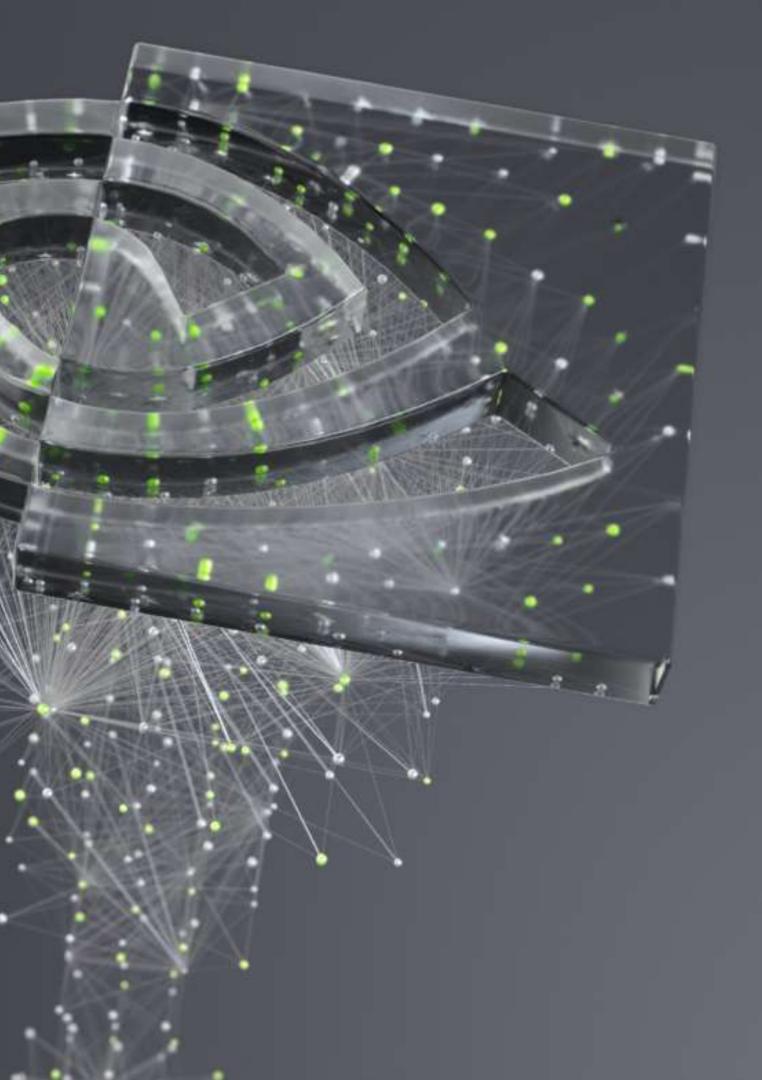
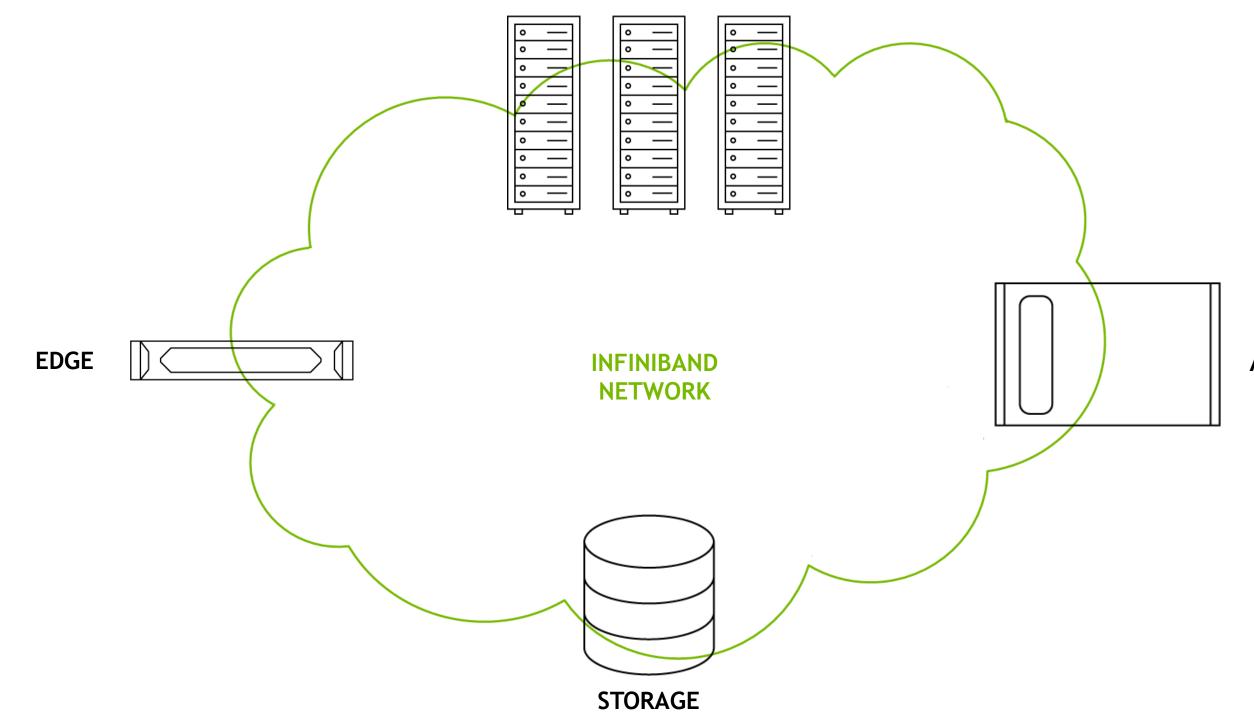


# INFINIBAND IN-NETWORK COMPUTING MUG, August 2020



# THE NEW SCIENTIFIC COMPUTING WORLD

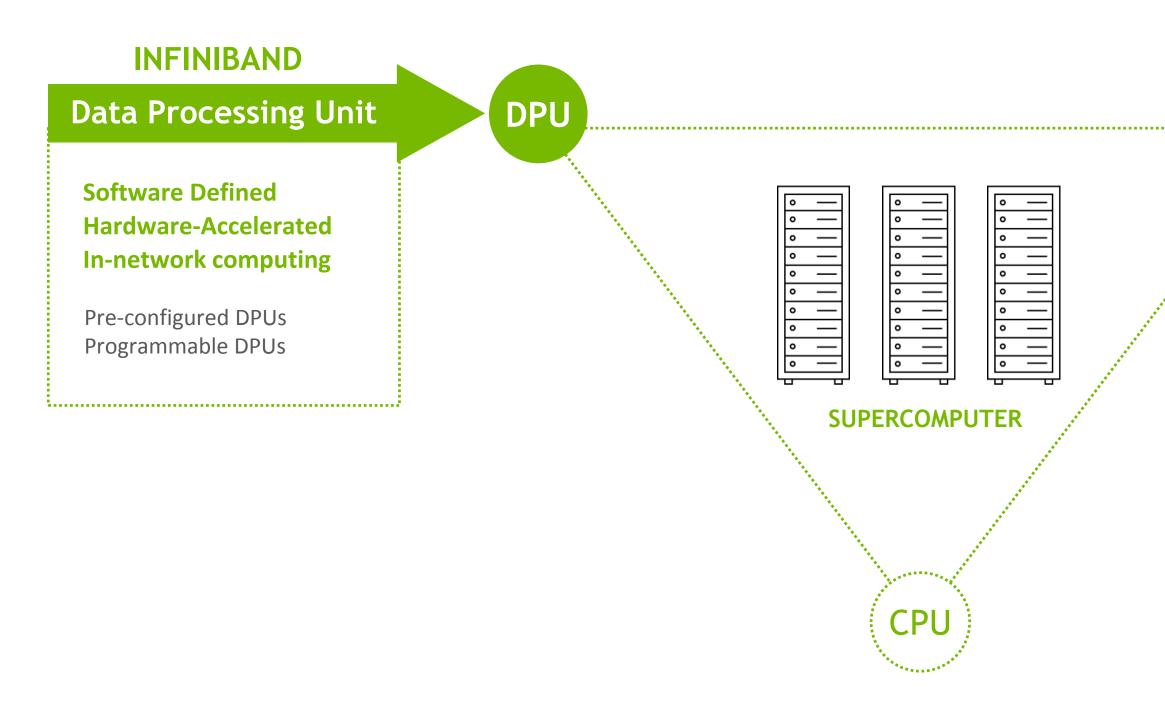
#### SUPERCOMPUTER

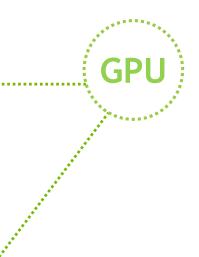


APPLIANCE



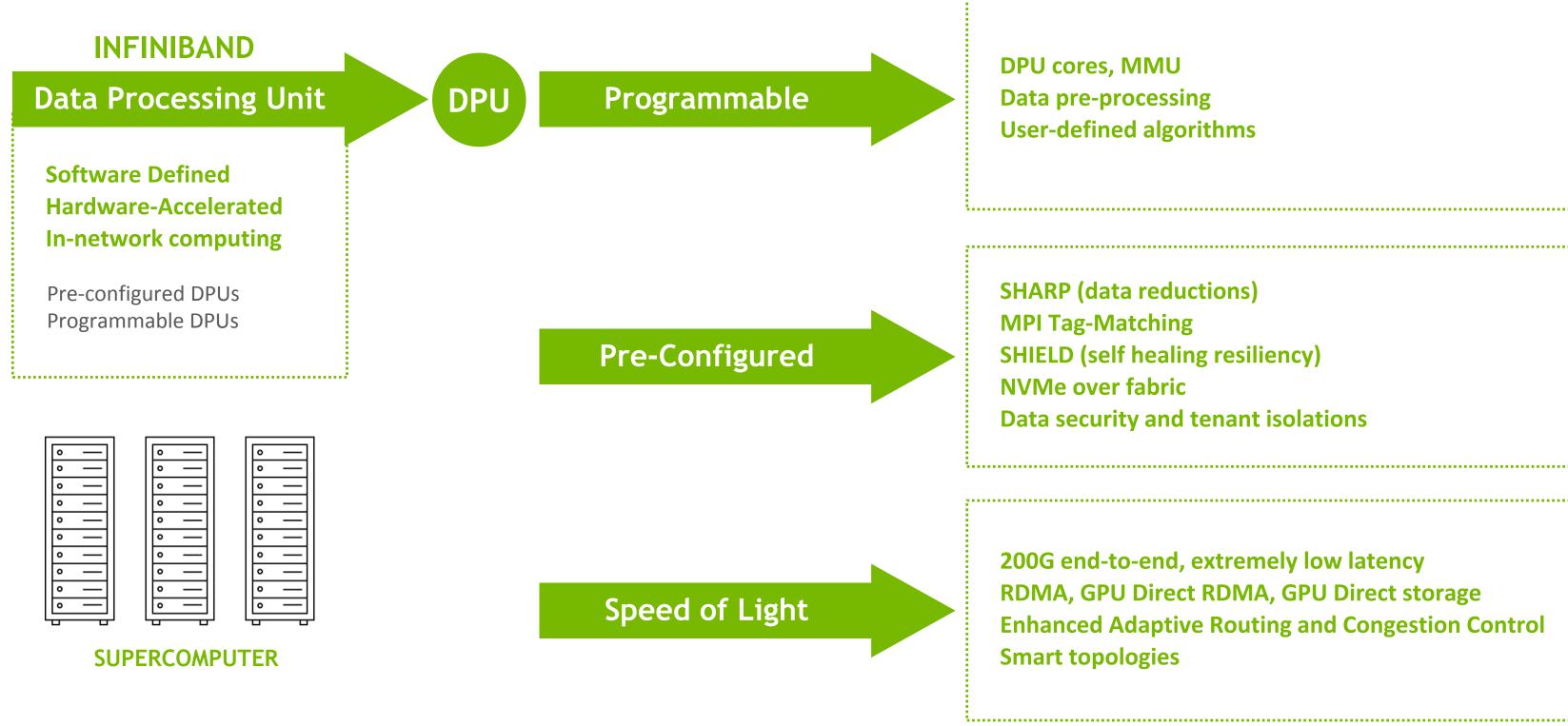
# IN-NETWORK COMPUTING ACCELERATED SUPERCOMPUTING







# **IN-NETWORK COMPUTING ACCELERATED SUPERCOMPUTING**



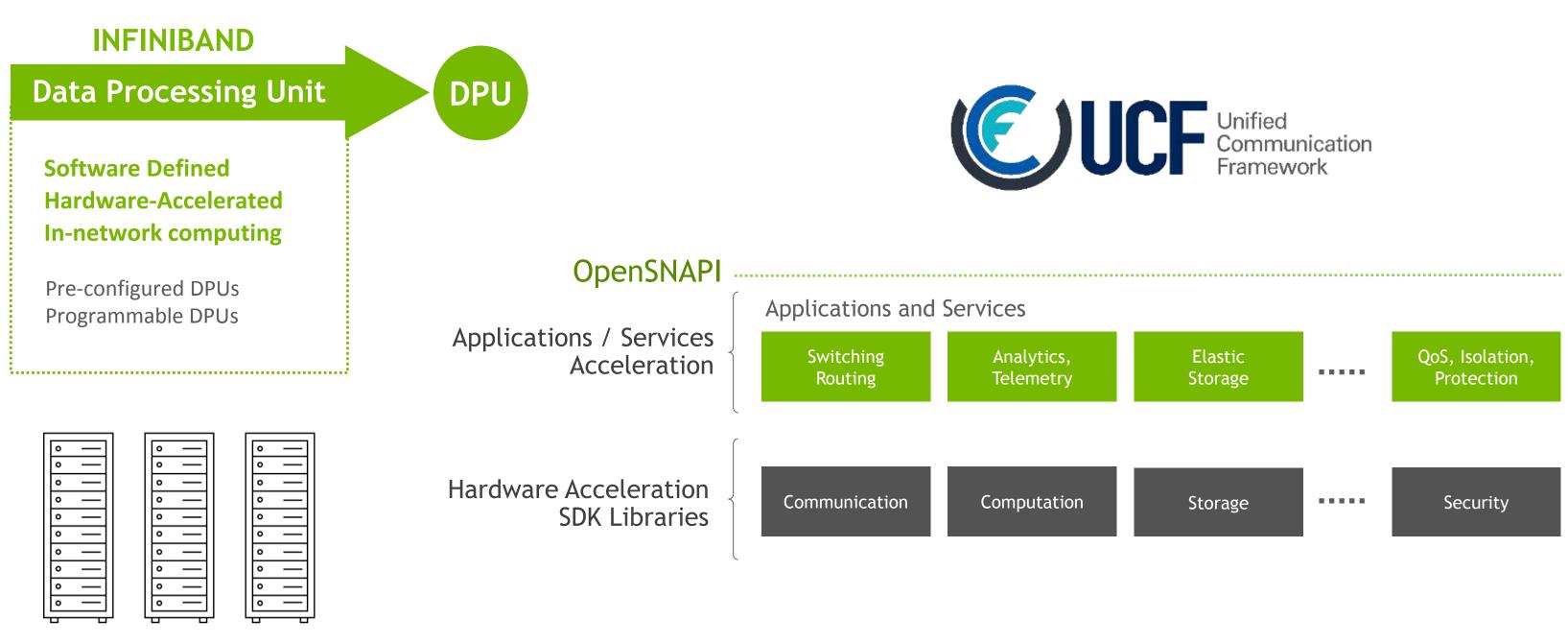
**DPU cores, MMU Data pre-processing User-defined algorithms** 

- **SHARP (data reductions)**
- **MPI Tag-Matching**
- **SHIELD (self healing resiliency)**
- NVMe over fabric
- Data security and tenant isolations

200G end-to-end, extremely low latency **RDMA, GPU Direct RDMA, GPU Direct storage Enhanced Adaptive Routing and Congestion Control Smart topologies** 



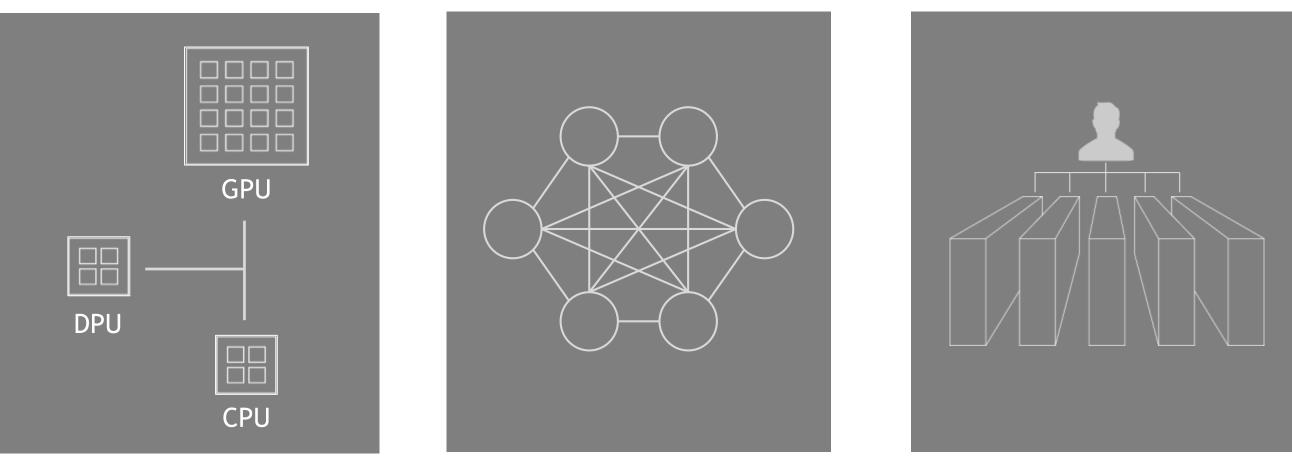
# IN-NETWORK COMPUTING ACCELERATED SUPERCOMPUTING



SUPERCOMPUTER



# INFINIBAND TECHNOLOGY FUNDAMENTALS



Smart End-Point

Architected to Scale

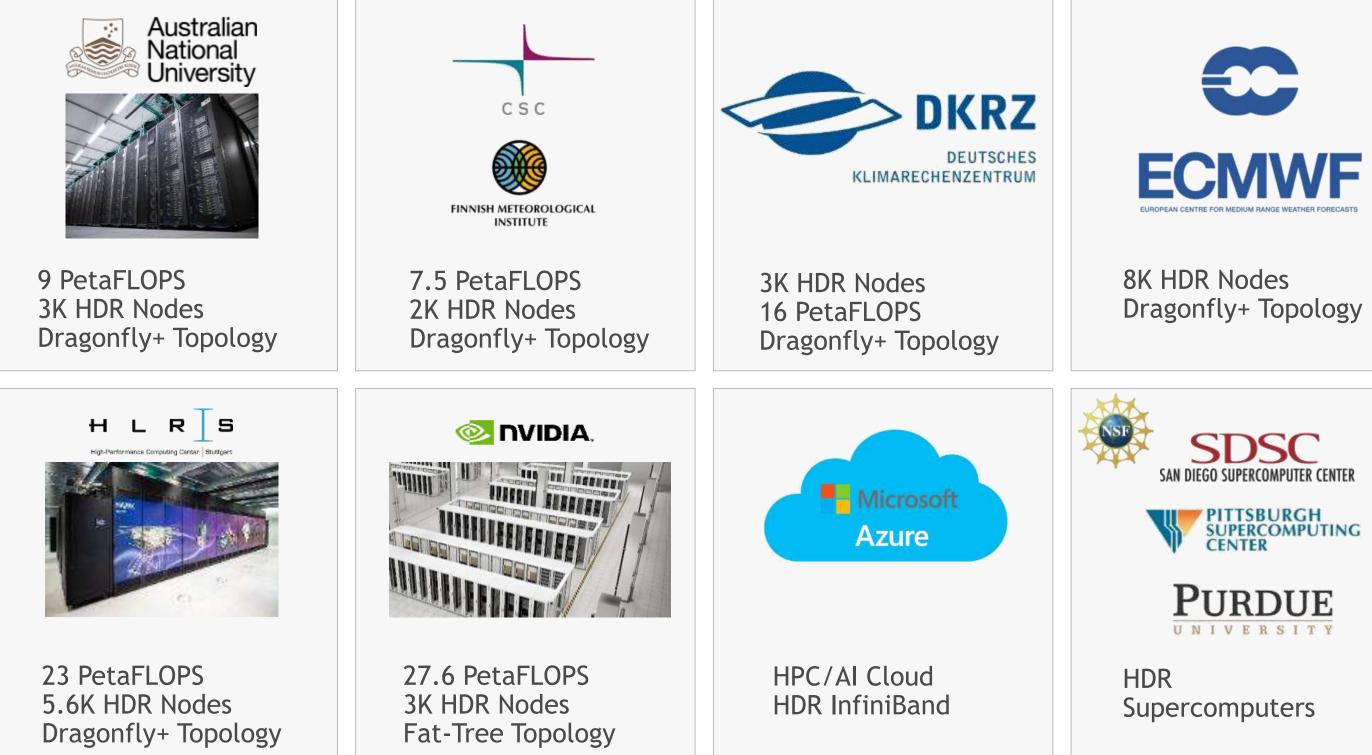
### Centralized Management



### Standard



# HDR 200G INFINIBAND ACCELERATES NEXT GENERATION HPC AND AI SUPERCOMPUTERS (EXAMPLES)





35.5 PetaFLOPS **2K HDR Nodes** Fat-Tree Topology



23.5 PetaFLOPS **8K HDR Nodes** Fat-Tree Topology



≥ NVIDIA



Scalable Hierarchical Aggregation and **Reduction Protocol** 

# SCALABLE HIERARCHICAL AGGREGATION AND **REDUCTION PROTOCOL** (SHARP)

# SCALABLE HIERARCHICAL AGGREGATION AND REDUCTION **PROTOCOL (SHARP)**

In-network Tree based aggregation mechanism

Multiple simultaneous outstanding operations

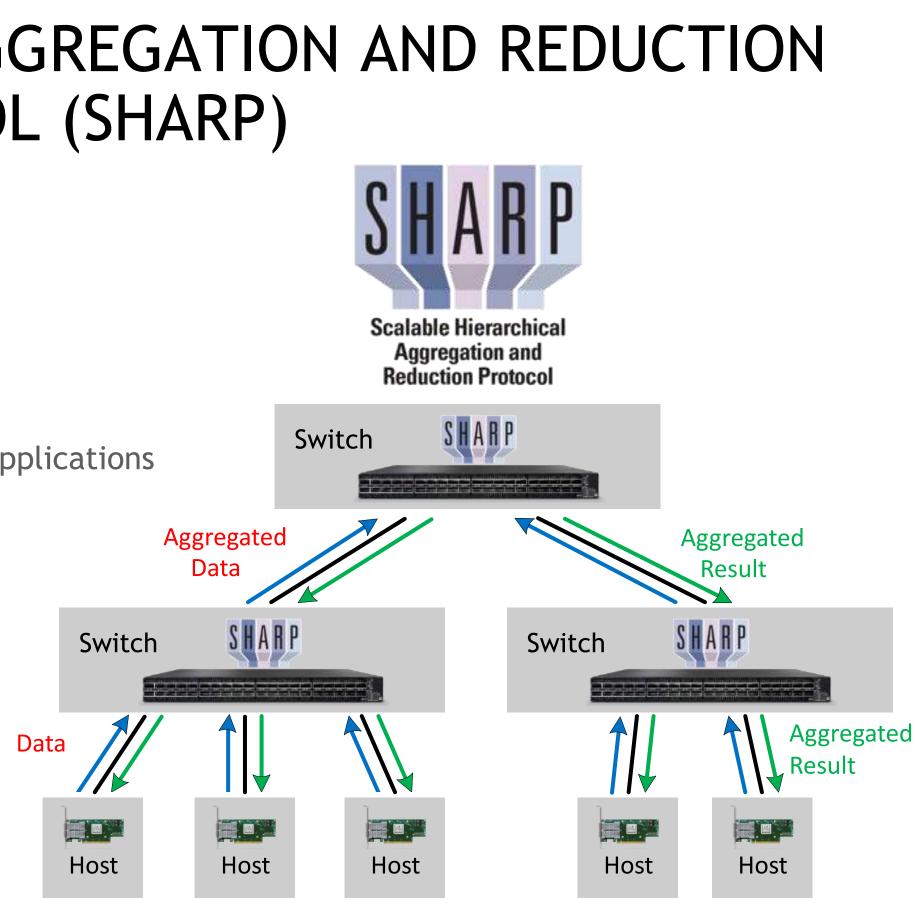
For HPC (MPI / SHMEM) and Distributed Machine Learning applications

Scalable High Performance Collective Offload

Barrier, Reduce, All-Reduce, Broadcast and more

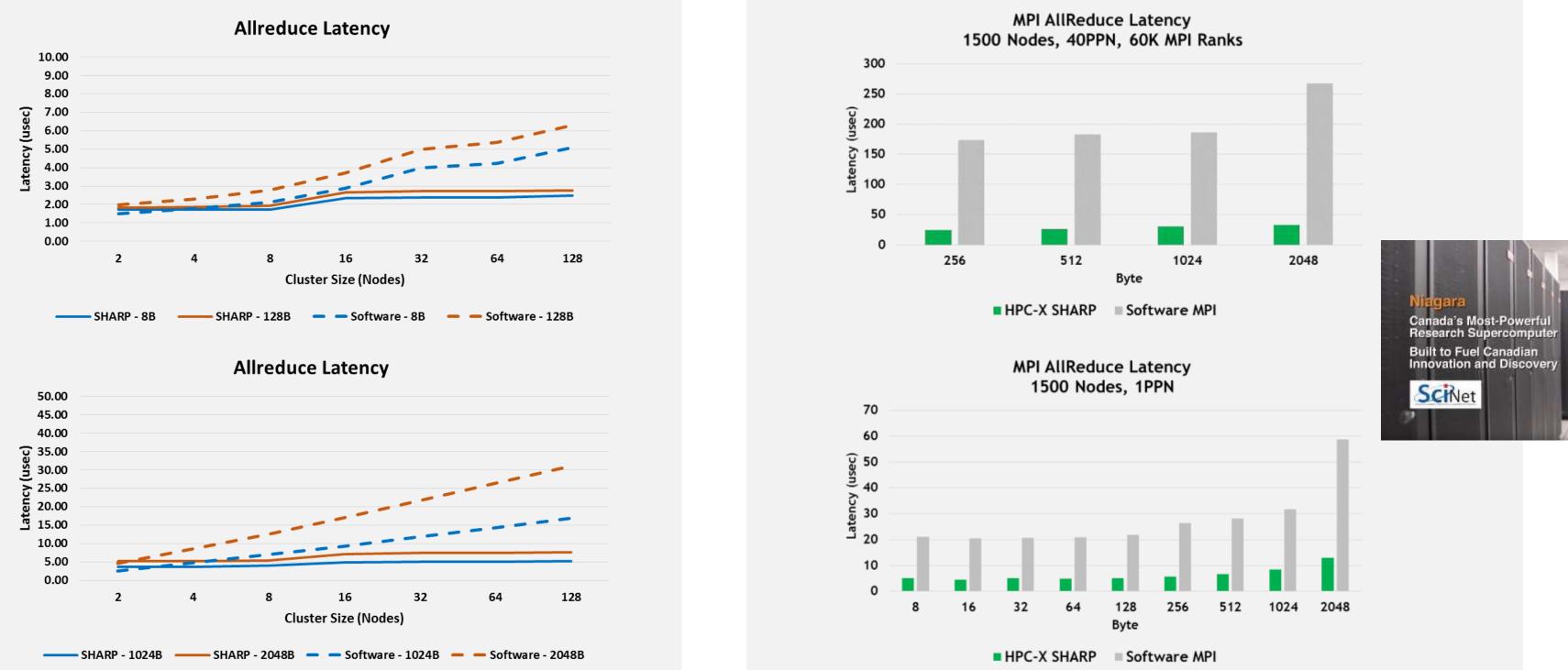
Sum, Min, Max, Min-loc, max-loc, OR, XOR, AND

Integer and Floating-Point, 16/32/64 bits





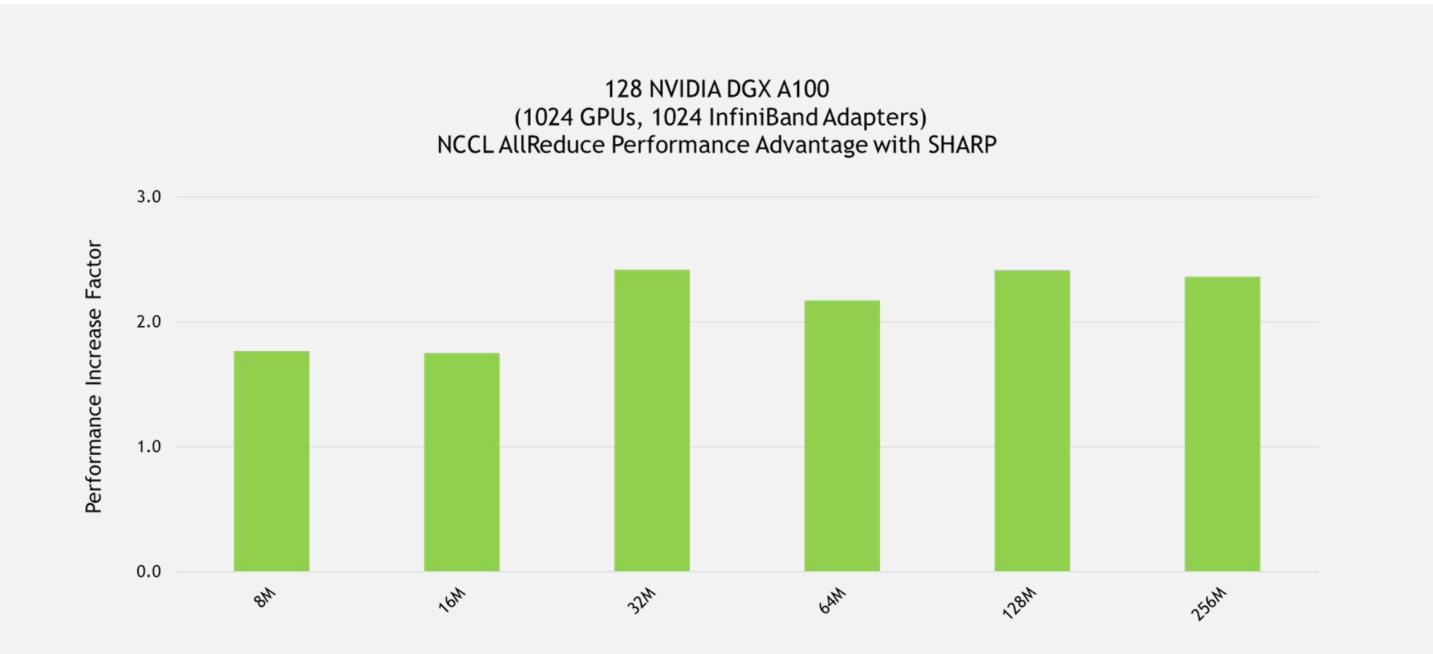
### SHARP ALLREDUCE PERFORMANCE ADVANTAGES Providing Flat Latency, 7X Higher Performance



10



### INFINIBAND SHARP AI PERFORMANCE ADVANTAGE 2.5X Higher Performance



Message Size (B)

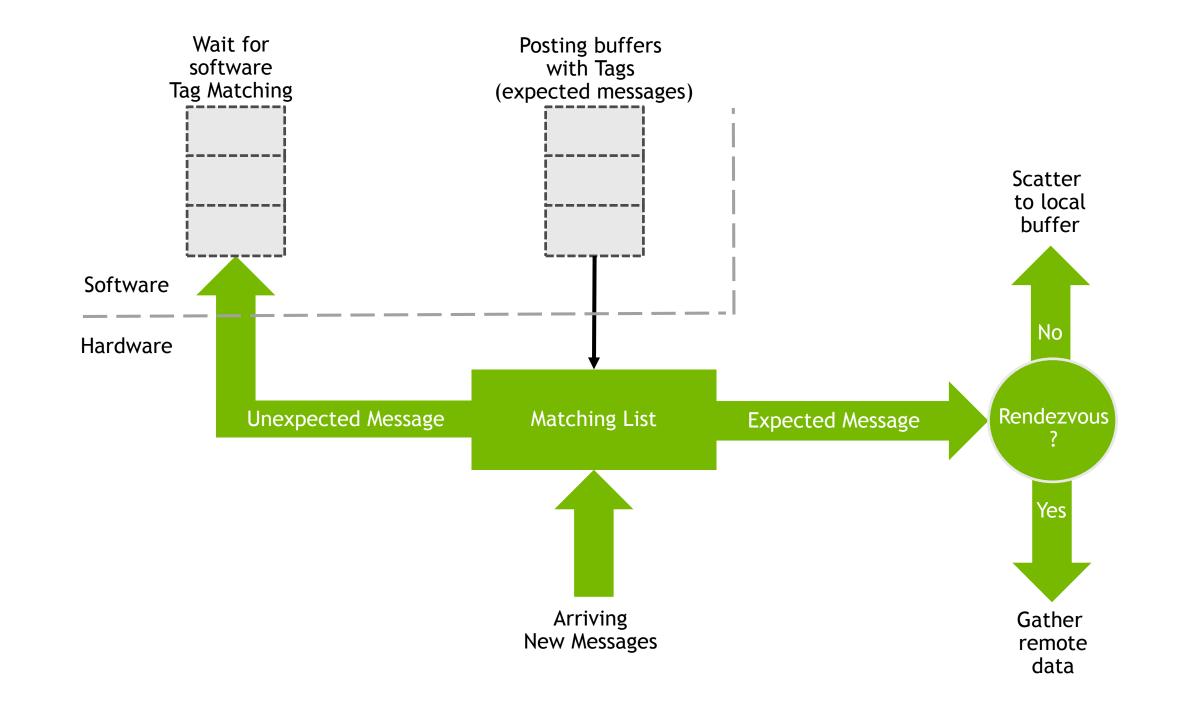






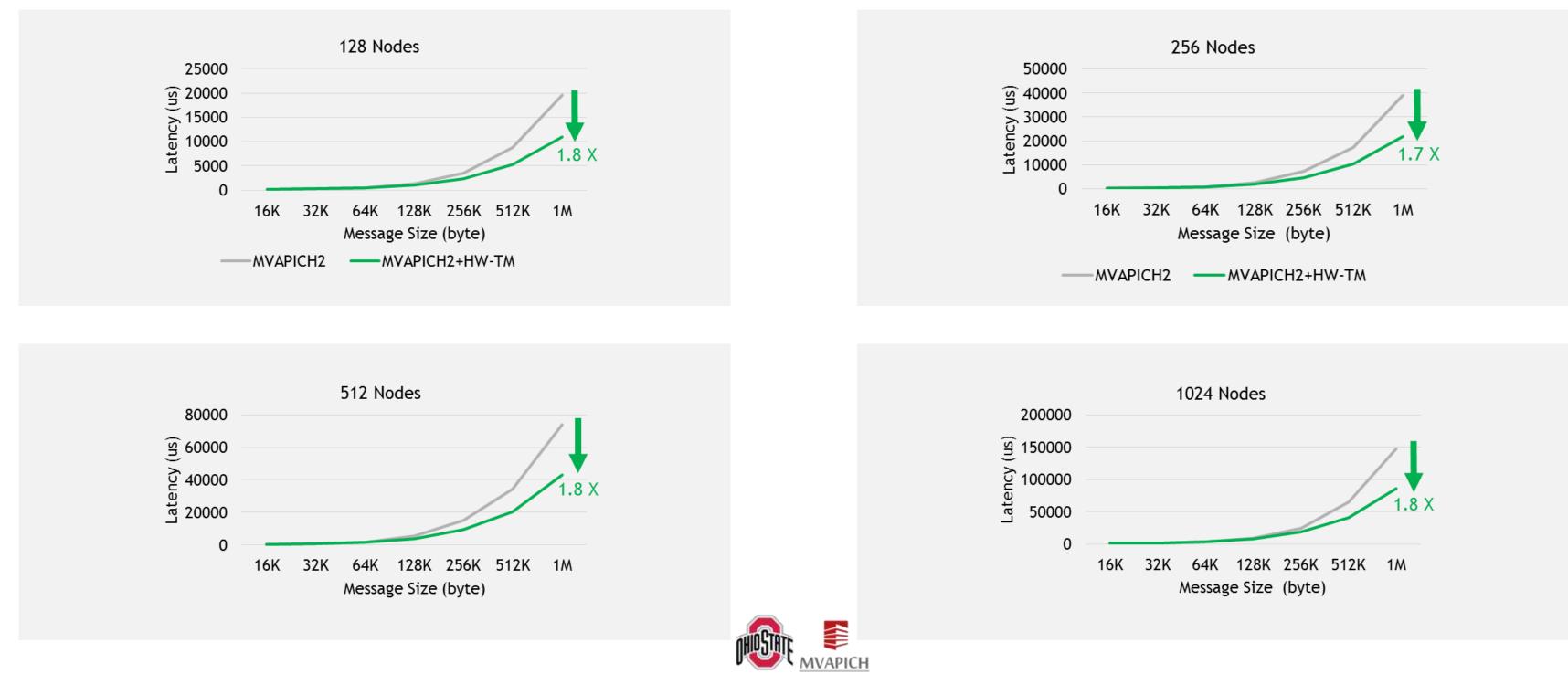
# MPI TAG MATCHING HARDWARE ENGINE

# INFINIBAND MPI TAG MATCHING HARDWARE ENGINE





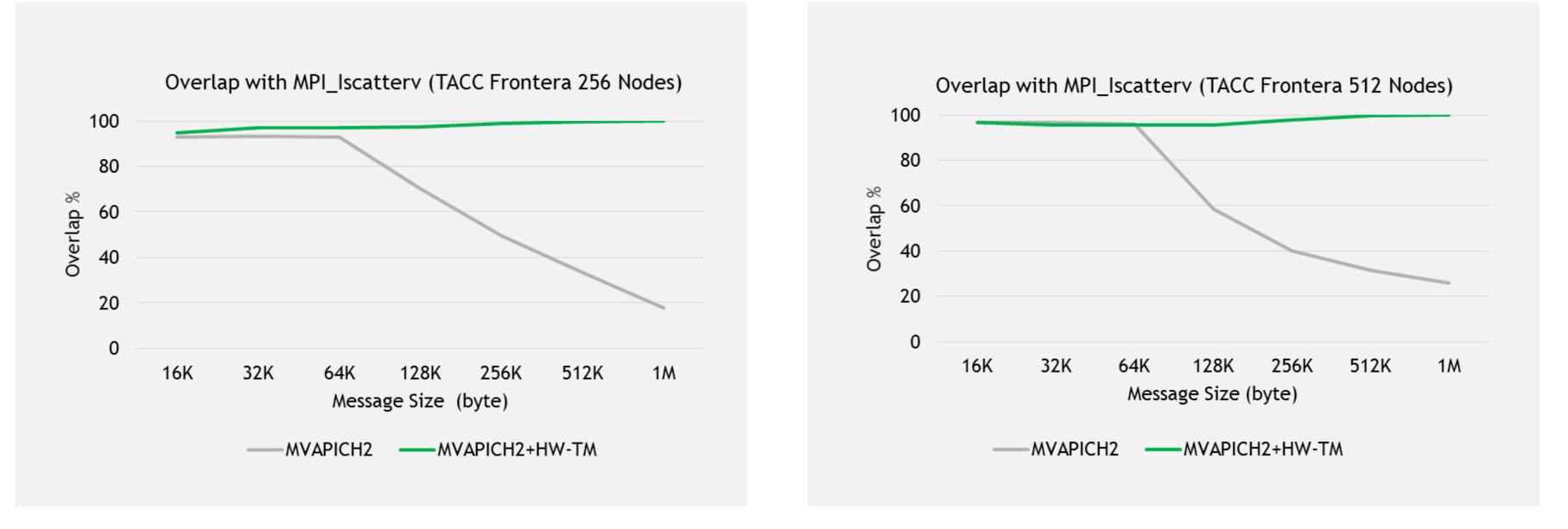
## HARDWARE TAG MATCHING PERFORMANCE ADVANTAGES **1.8X Higher MPI\_Iscatterv Performance on TACC Frontera**



Courtesy of Dhabaleswar K. (DK) Panda Ohio State University



## HARDWARE TAG MATCHING PERFORMANCE ADVANTAGES Nearly 100% Compute - Communication Overlap



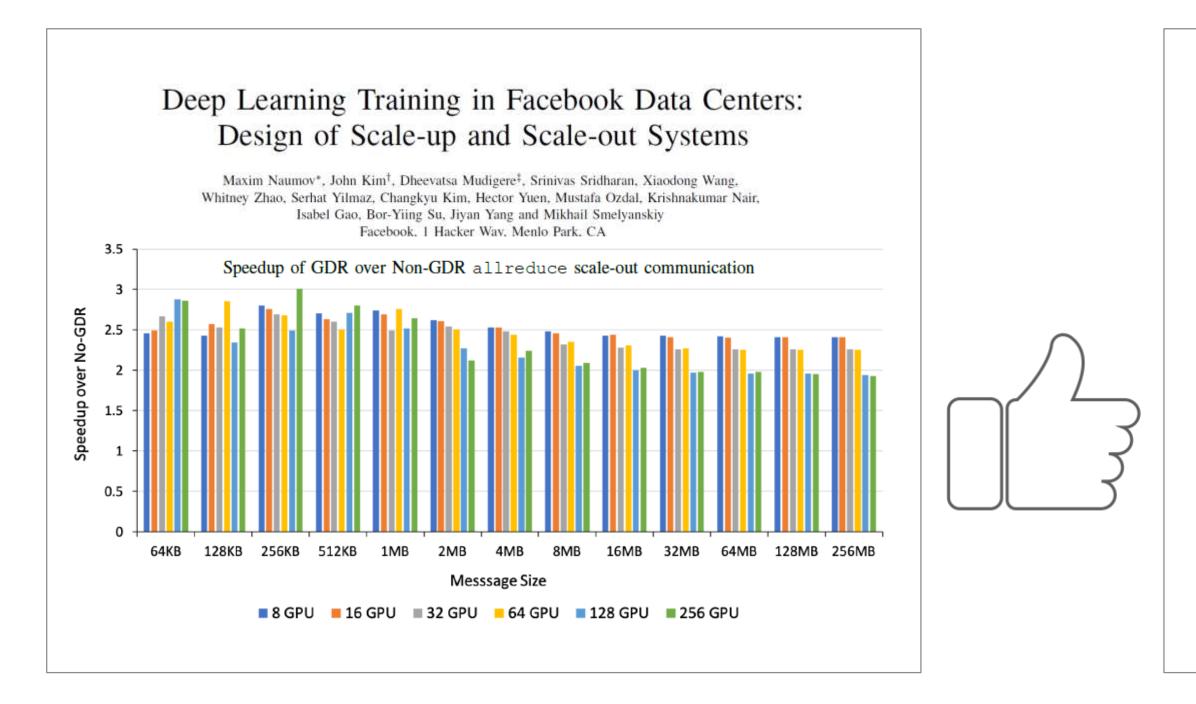


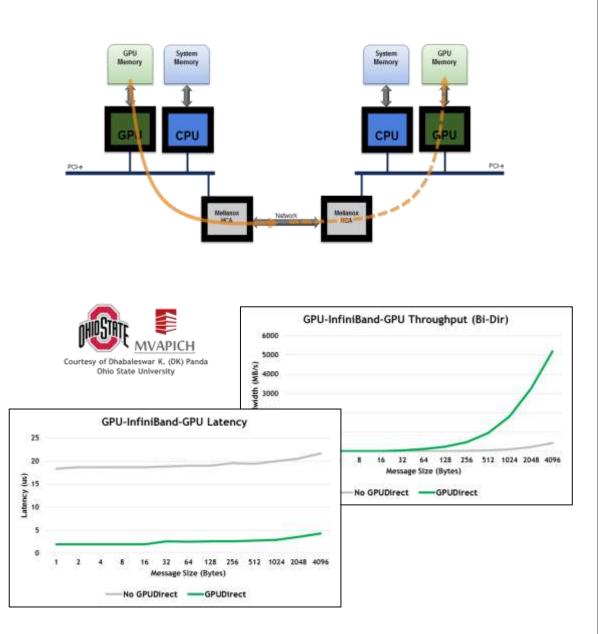




# GPUDIRECT

## EFFICIENT COMMUNICATION FOR ACCELERATED TRAINING 10X Better Latency & Bandwidth, 3X Faster Deep Learning





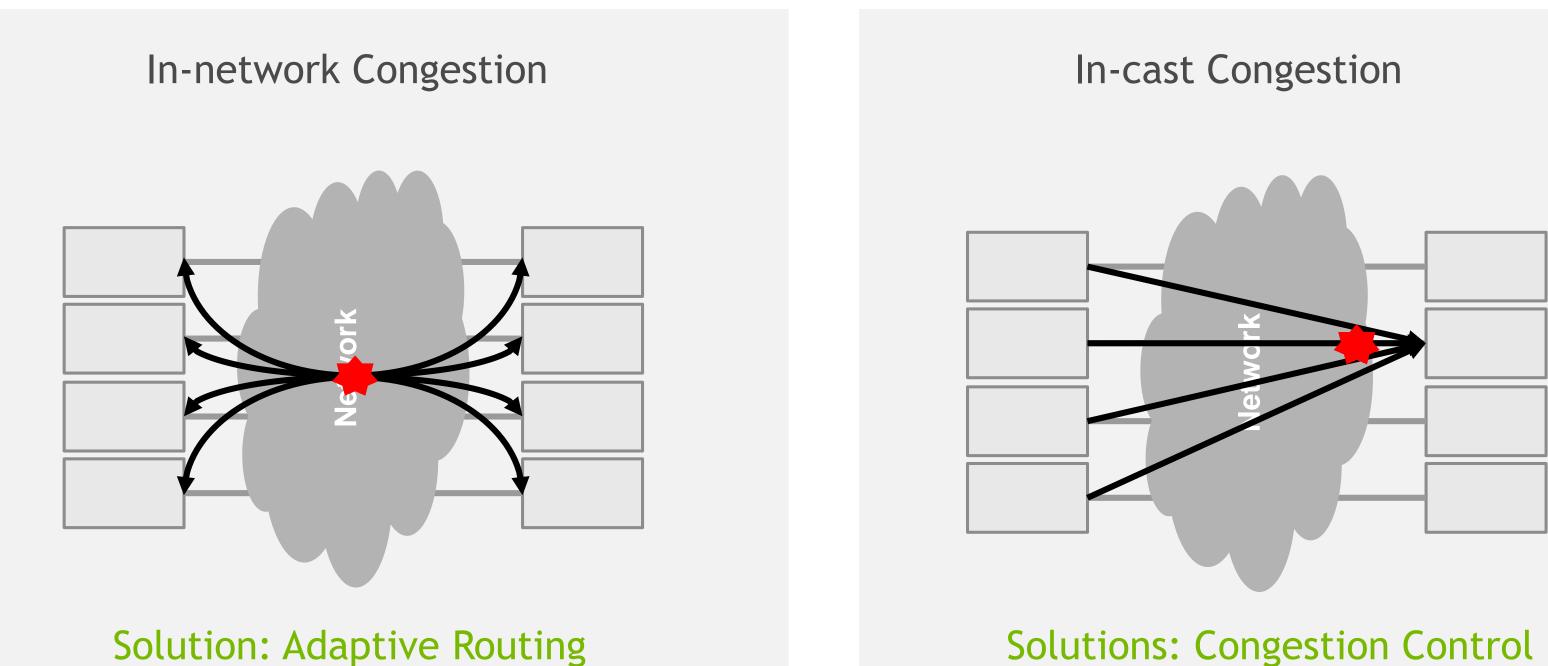
≥ NVIDIA

17



# QUALITY OF SERVICE

# NETWORK CONGESTION TYPES

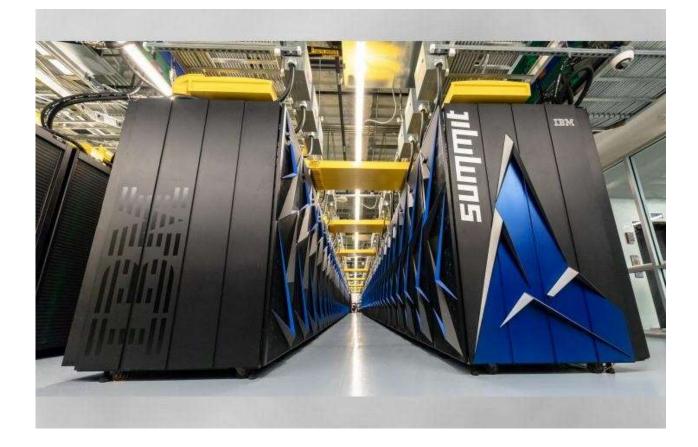


Solutions: Congestion Control

19

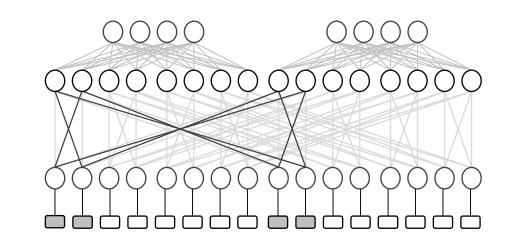
≥ NVIDIA

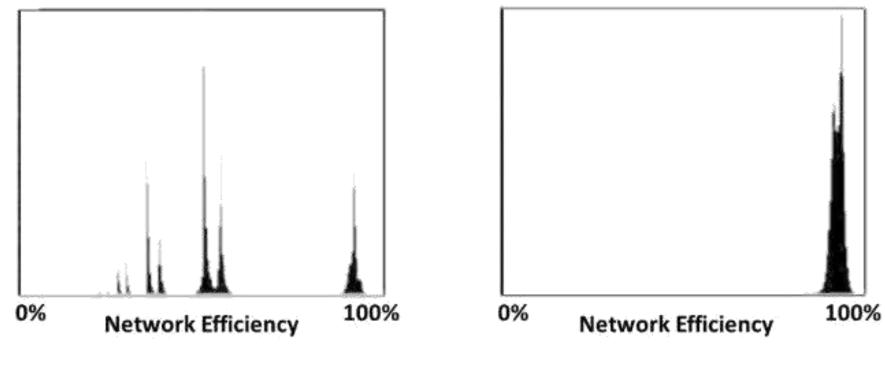
# **IN-NETWORK CONGESTION: ADAPTIVE ROUTING**



#### The Design, Deployment, and Evaluation of the **CORAL Pre-Exascale Systems**

Sudharshan S. Vazhkudai<sup>†</sup>, Bronis R. de Supinski<sup>‡</sup>, Arthur S. Bland<sup>†</sup>, Al Geist<sup>†</sup>, James Sexton<sup>\*</sup>, Jim Kahle<sup>\*</sup>, Christopher J. Zimmer<sup>†</sup>, Scott Atchley<sup>†</sup>, Sarp Oral<sup>†</sup>, Don E. Maxwell<sup>†</sup>, Veronica G. Vergara Larrea<sup>†</sup>, Adam Bertsch<sup>‡</sup>, Robin Goldstone<sup>‡</sup>, Wayne Joubert<sup>†</sup>, Chris Chambreau<sup>‡</sup>, David Appelhans<sup>\*</sup>, Robert Blackmore<sup>\*</sup>, Ben Casses<sup>‡</sup>, George Chochia<sup>\*</sup>, Gene Davison<sup>\*</sup>, Matthew A. Ezell<sup>†</sup>, Tom Gooding<sup>\*</sup>, Elsa Gonsiorowski<sup>‡</sup>, Leopold Grinberg\*, Bill Hanson\*, Bill Hartner\*, Ian Karlin<sup>‡</sup>, Matthew L. Leininger<sup>‡</sup>, Dustin Leverman<sup>†</sup>, Chris Marroquin\*, Adam Moody<sup>‡</sup>, Martin Ohmacht\*, Ramesh Pankajakshan<sup>‡</sup>, Fernando Pizzano\*, James H. Rogers<sup>†</sup>, Bryan Rosenburg<sup>\*</sup>, Drew Schmidt<sup>†</sup>, Mallikarjun Shankar<sup>†</sup>, Feiyi Wang<sup>†</sup>, Py Watson<sup>‡</sup>, Bob Walkup\*, Lance D. Weems<sup>‡</sup>, Junqi Yin<sup>†</sup> <sup>†</sup> Oak Ridge National Laboratory, <sup>‡</sup> Lawrence Livermore National Laboratory, <sup>\*</sup> IBM {vazhkudaiss@ornl.gov, bronis@llnl.gov}





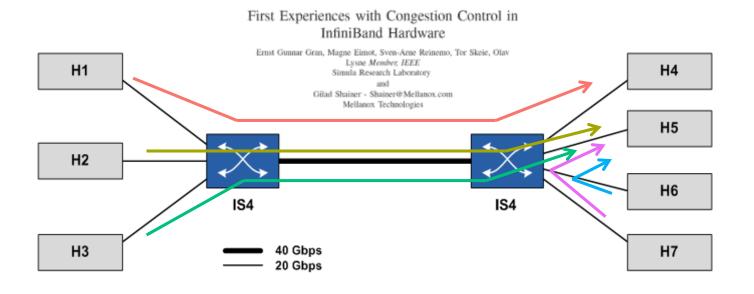
Static Routing

### mpiGraph: Static vs. Adaptive Routing

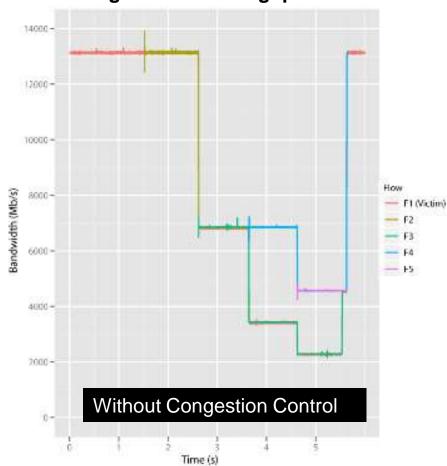
Adaptive Routing



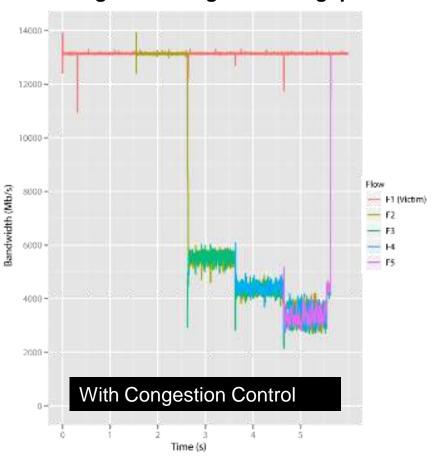
# INFINIBAND CONGESTION CONTROL

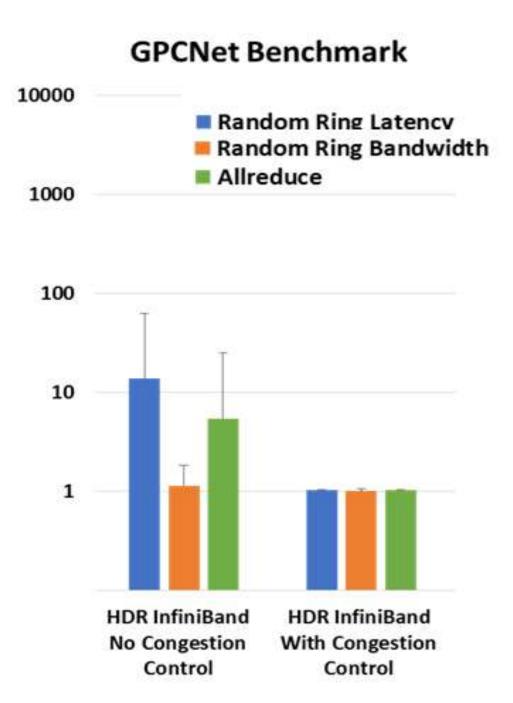


Congestion – Throughput loss



No congestion – highest throughput!





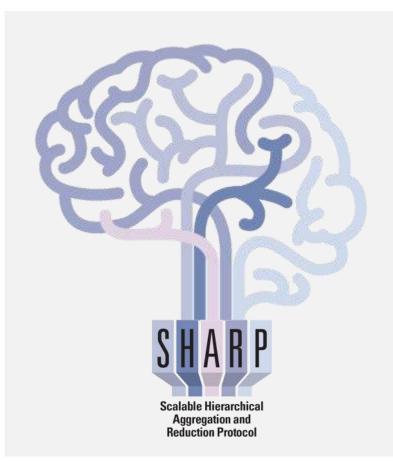


# INFINIBAND ACCELERATED SUPERCOMPUTING



### Speed of Light

200Gb/s Data Throughput RDMA and GPUDirect RDMA 3X Better (Lower) Latency



### SHARP AI Technology

Al Acceleration Engines 2.5X Higher Al Performance



SHIELD AI Technology Self Healing Network 1000X Faster Recovery Time



UFM Cyber AI Data Center Cyber Intelligence and Analytics

